EC 131	701 (agid tog (see
927	el purint in the land
Roll No. o	f candidate
WEE Inch	2018
B.Tec	ch. 7th Semester End-Term Examination
	LINEAR INTEGRATED CIRCUITS
Full Mark	rs - 100 Time - Three hours
F773	teonal lactification
The	e figures in the margin indicate full marks for the questions.
	for the questions.
	GROUP A
	Answer ALL questions.
1. Choo	ose the correct answer: $\frac{1}{10}$ $\frac{1}{10}$ $\frac{1}{10}$ $\frac{1}{10}$ $\frac{1}{10}$ $\frac{1}{10}$
(i)	An unity gain amplifier is also known a
\ - /	(difference amplifier/ comparato
	single ended / voltage follower)
(ii)	If the (+) terminal of an inverting op-amp is connected to the ground the (-) terminal wis (not need an input resistor/ b
	virtual ground / have high reverse current / no
	Invert the signal)
(iii)	Differential gain and common-mode gain rational design and common-mode gain design and common
	is called as ——— (Amplitude / Differential mode rejection / Common mode rejection Phase)
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